Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799
Filing Date: Oct. 3, 2003 Atty. Tel: (415) 318-1160 Sheet 1 of 8



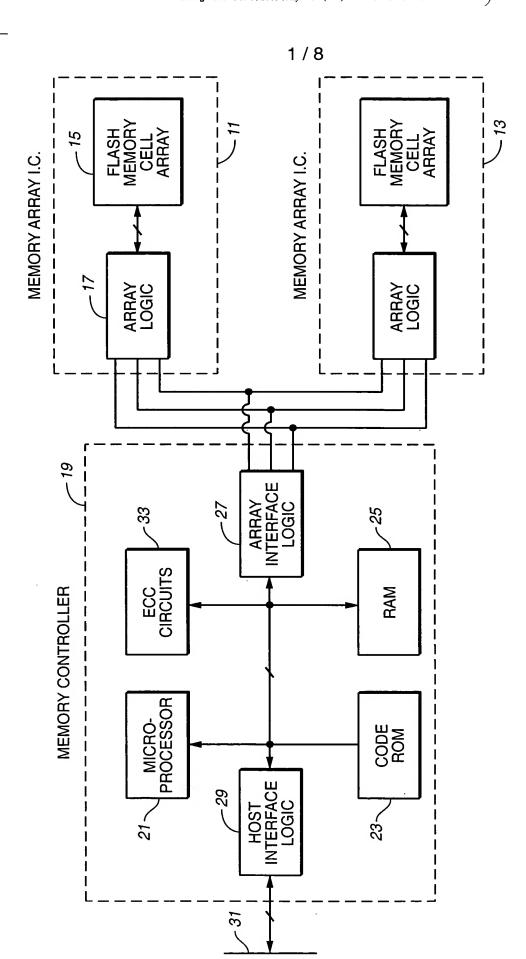
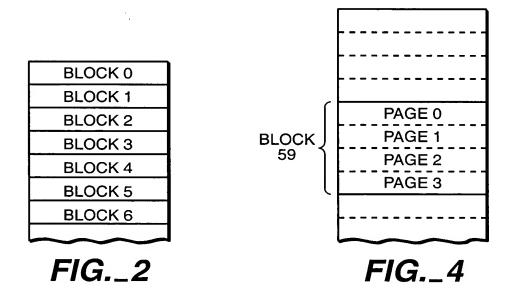
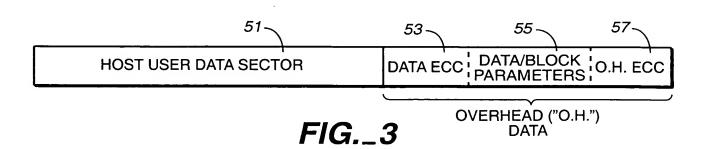


FIG._ 1A

Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799
Filing Date: Oct. 3, 2003 Atty. Tel: (415) 318-1160 Sheet 2 of 8

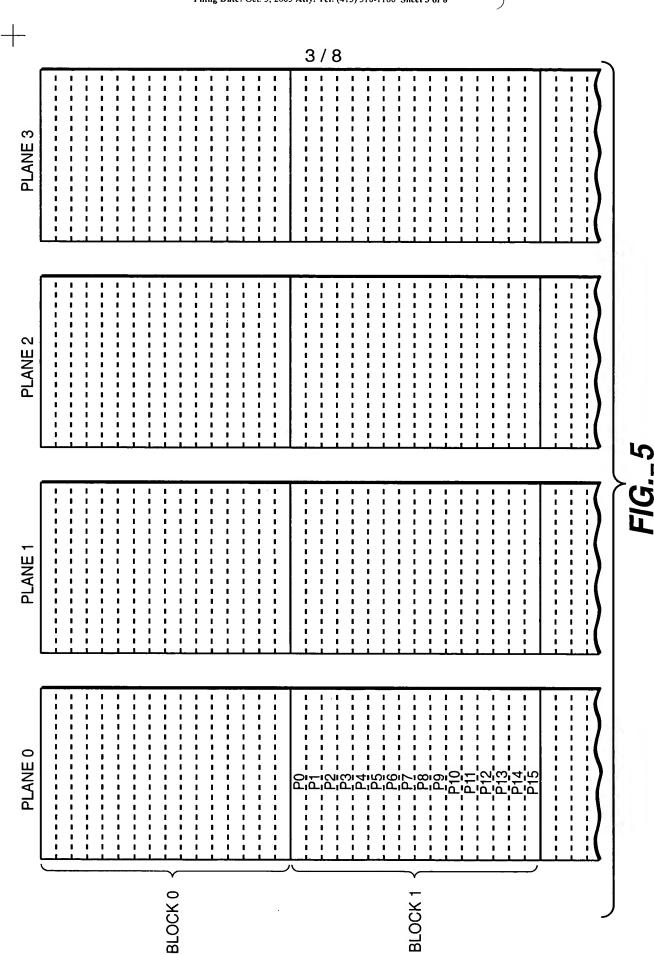
2/8 **SYSTEM** 41-37 MICRO-DATA **PROCESSOR RAM** 35 31'-**MEMORY INTERFACE LOGIC OTHER** 39-43 CODE **CIRCUITS AND ROM** SUBSYSTEMS FIG._1B



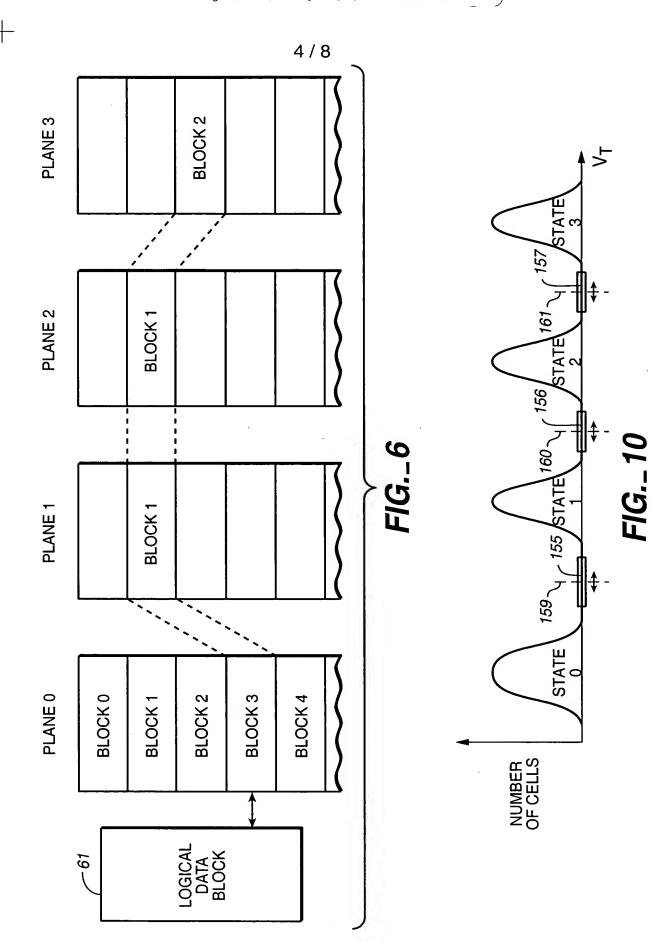


-

Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799
Filing Date: Oct. 3, 2003 Atty. Tel: (415) 318-1160 Sheet 3 of 8



Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799
Filing Date: Oct. 3, 2003 Atty. Tel: (415) 318-1160 Sheet 4 of 8



Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799
Filing Date: Oct. 3, 2003 Atty. Tel: (415) 318-1160 Sheet 5 of 8

5/8

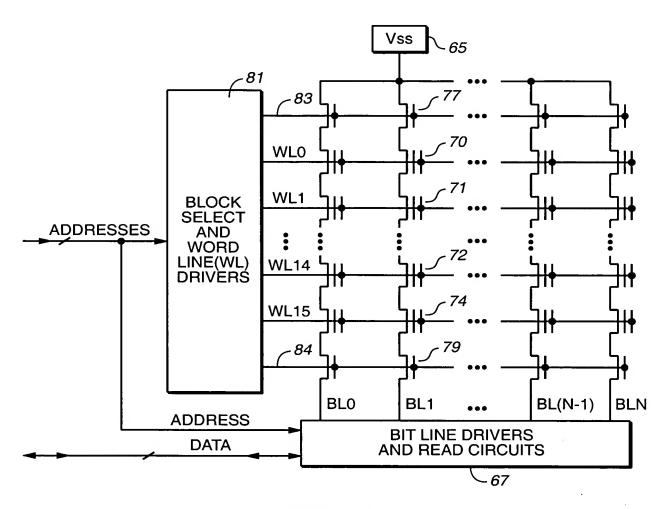
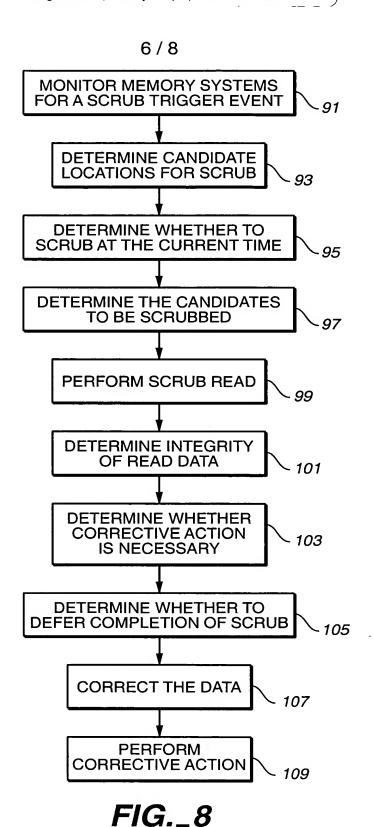


FIG._7

+



1

Appln. No.: 10/678,345 Docket No.: SNDK.241US0
Title: Flash Memory Data Correction and Scrub Techniques
Inventors: Gonzalez et al. Conf. No.: 1799

